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01/16/02

72029

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10053138	01/18/2002	438		1765	

\*\*APPLICANTS: Savas Stephen;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/860,698 05/16/2001 PAT 6,395,641 \*  
WHICH IS A DIV OF 09/398,553 09/17/1999 PAT 6,253,704  
WHICH IS A CON OF 08/727,209 10/08/1996 PAT 5,983,828  
WHICH CLAIMS BENEFIT OF 60/005,288 10/13/1995

(\*)Data provided by applicant is not consistent with PTO records.

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\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

14912.786

TITLE : Pulsed plasma processing of semiconductor substrates

U.S. DEPT. OF COMH./PAT. & TM-PTO-435L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Applicant Examiner</b>	
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